

I		II		This new version	
12		14		16	
Generation					
Name	Quickly Scheduler	DLD		On-line DLD	
System Type	Off-line	On-line, Real Time		On-line, Real Time	
Algorithm Type	Heuristic Algorithm	Multi-Priority Factor Multi-Dispatching Algorithm		Multi-Priority Factor Multi-Dispatching Algorithm	
Take Care MPS	No	Yes		Yes	
Take Care Lot Due Date	Yes	Yes		Yes	
Dispatching Subject	Production Lot	Production Lot		Production Lot and Engineering Lot	
Dispatching Subject Type	Wafer	Wafer		Wafer and PKG	
Dispatching Subject Stage	Test-INV/Sort1/Sort2	Sort1/Sort2		Test-INV/Sort1/Sort2/FT- BANK/F-Test/INKING	
Take Care Production	None	P/C		P/C, L/B for Wafer and L/B for PKG	
Production Apparatus Type	None	For single decide apparatus behavior		For multit-devices using the same apparatus behavior	
Scale in Taking Care Setup Time	Only take care the same PART ID to reduce setup time	Only take care the same PART ID to reduce setup time		Follow this sequence [Don't change test program] > [Don't change P/V]>{Don't change L/B}>[Don't change production type CP, FT] to	
Take Care Hardware and Software Special Constraint Information	None	Take care them as planning problem on exception rule system		Line PROMIS constraint function to take care them, and DLD exception rule system rule only for planning problem	
MES Environment	PROMIS	POSIDOM		PROMIS	
Permormance	Off-line system running 10 min, & manual tuning 4 hrs for all tester one day dispatching	Around 5 sec for one tester next dispatching lot FIG. 1		Around 7 sec for one tester next dispatching lot	

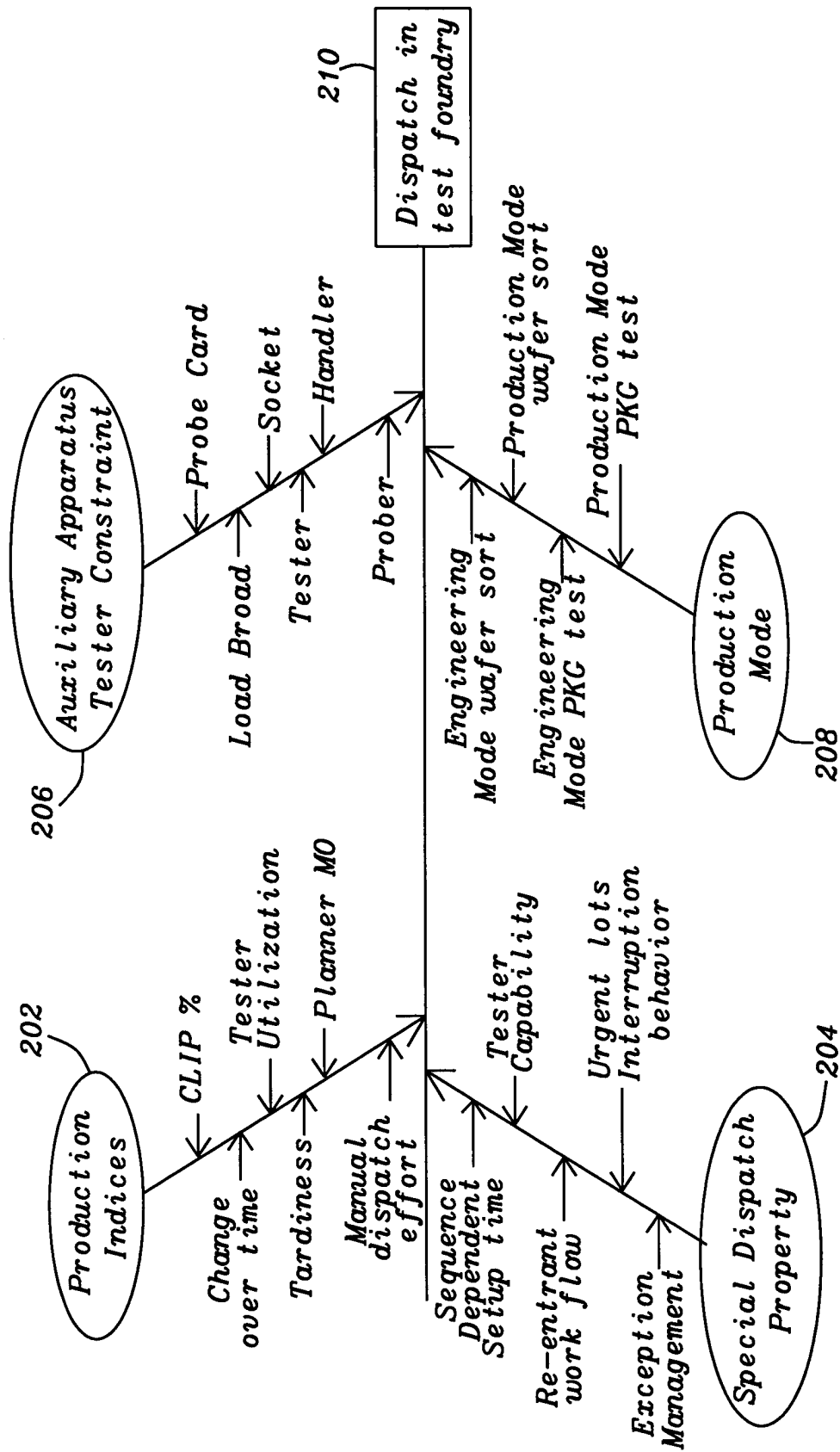
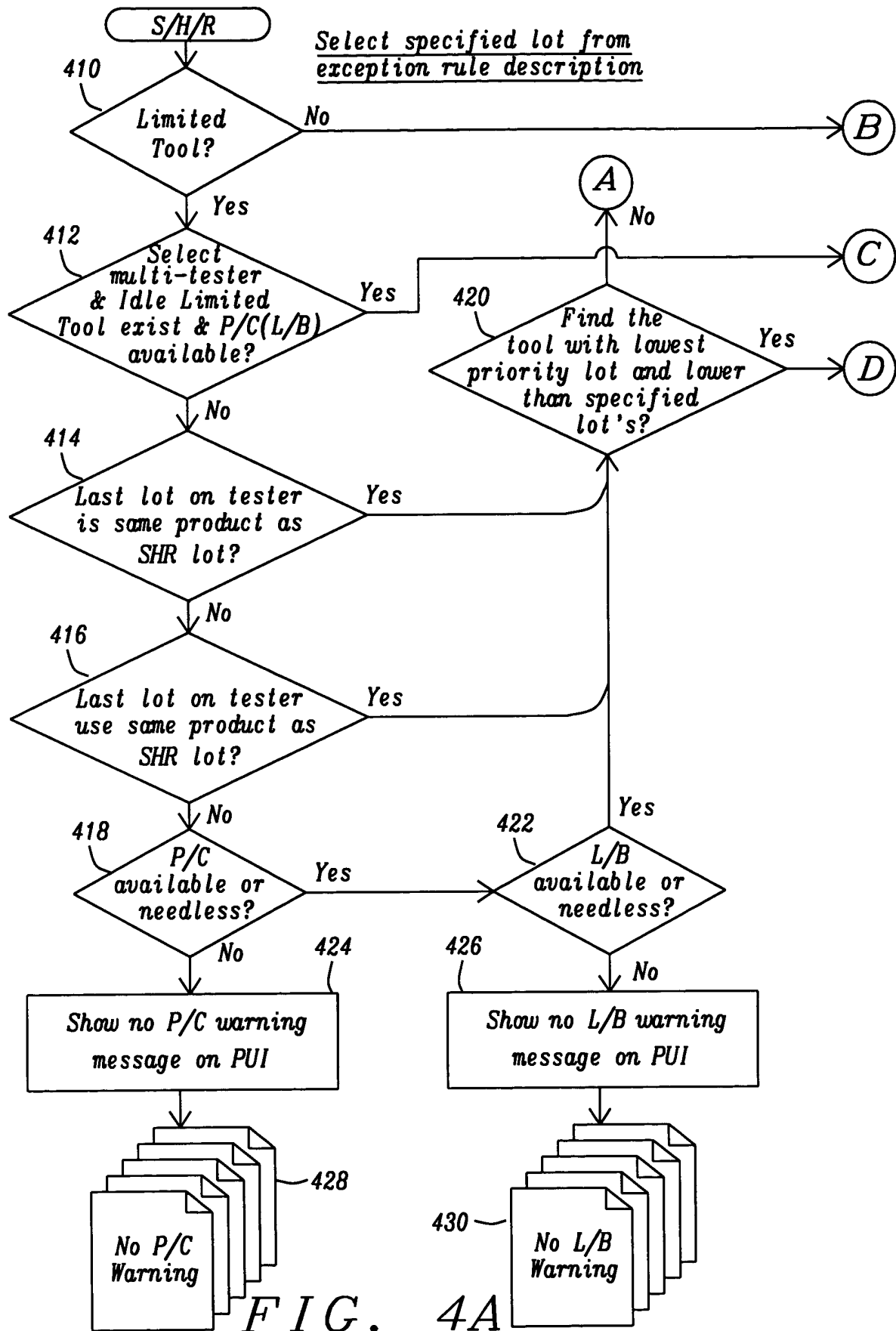


FIG. 2

308	304			Object
	Event Driven		Normal Lot and Eng. Lot	
300	SHR	HR		
Two-Phased Dispatch	Lot Rank	Lot Rank Priority Formula for Production Wafer/PKG		For Eng. Lot
		Priority = (Base * Weight)		
	Lot Assignment	Exception Lot Dispatching Rule	Exception Lot Dispatching Rule	Based on MPS Target to Reduce Setup Time
		SHR Dispatching Rule	Normal Lot & Eng. Lot Dispatching Rule	
Object	For SHR Dispatching	For HR Dispatching	For Normal Lot & Eng. Lot Dispatching	

FIG. 3



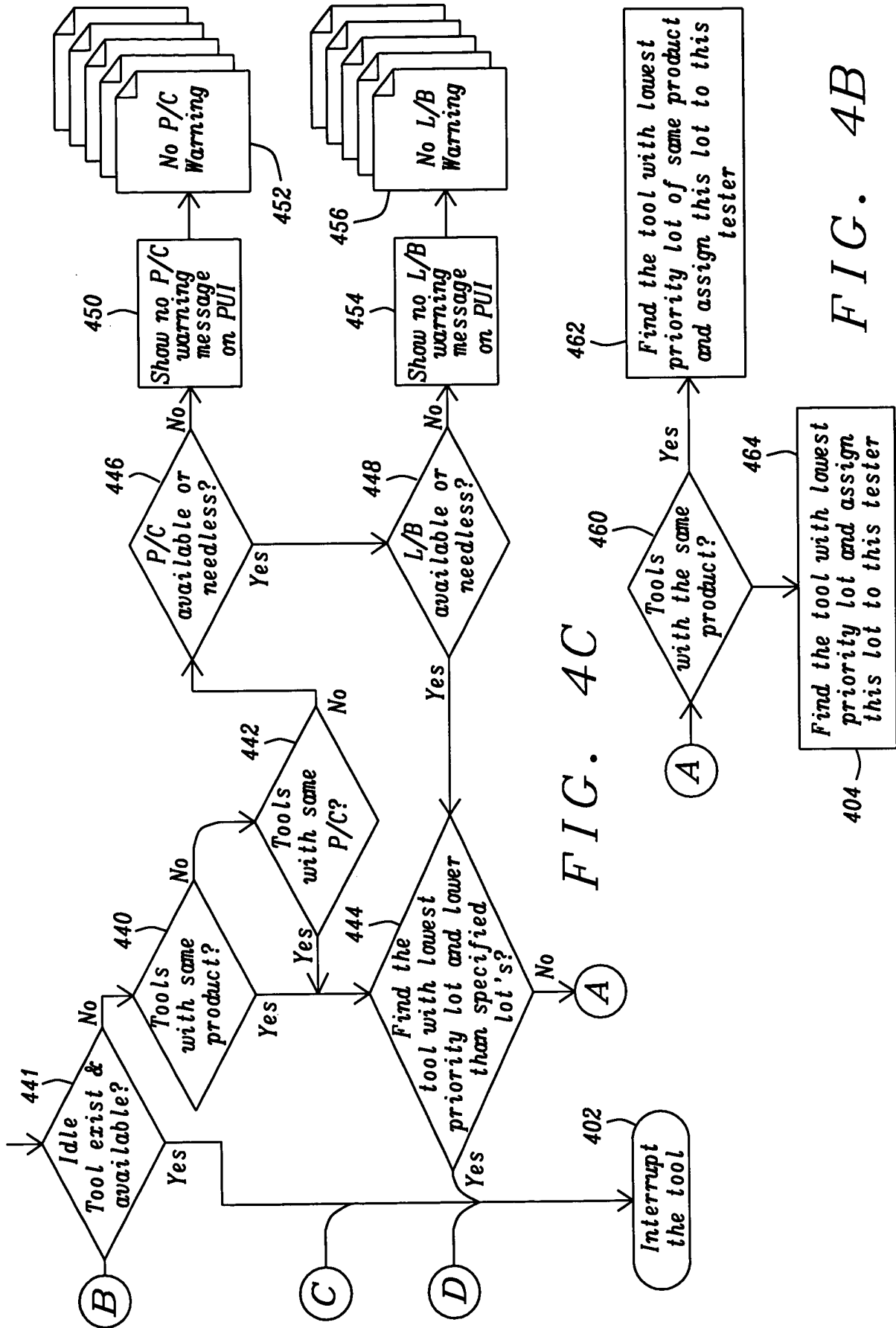


FIG. 4C

FIG. 4B

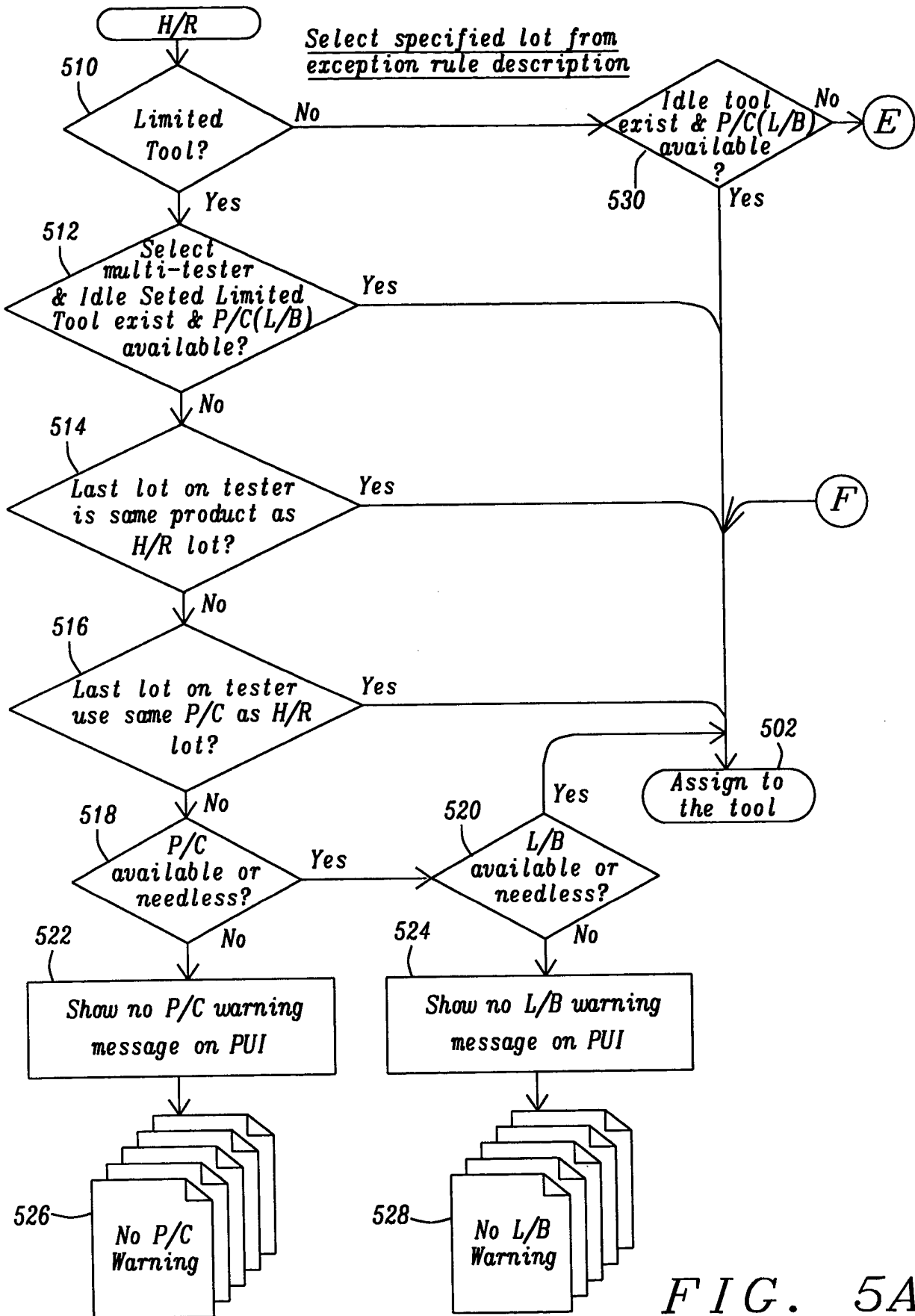


FIG. 5A

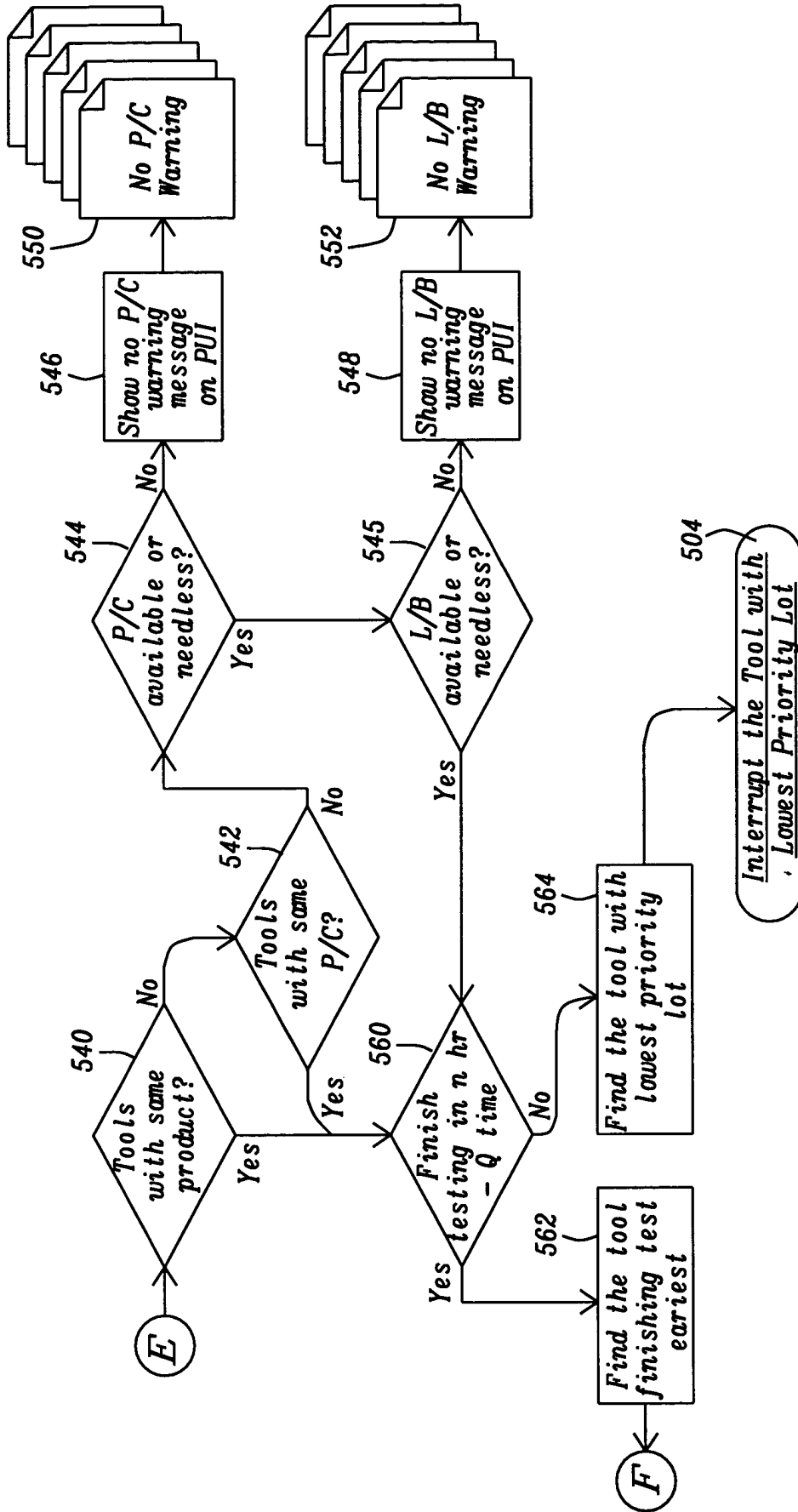


FIG. 5B

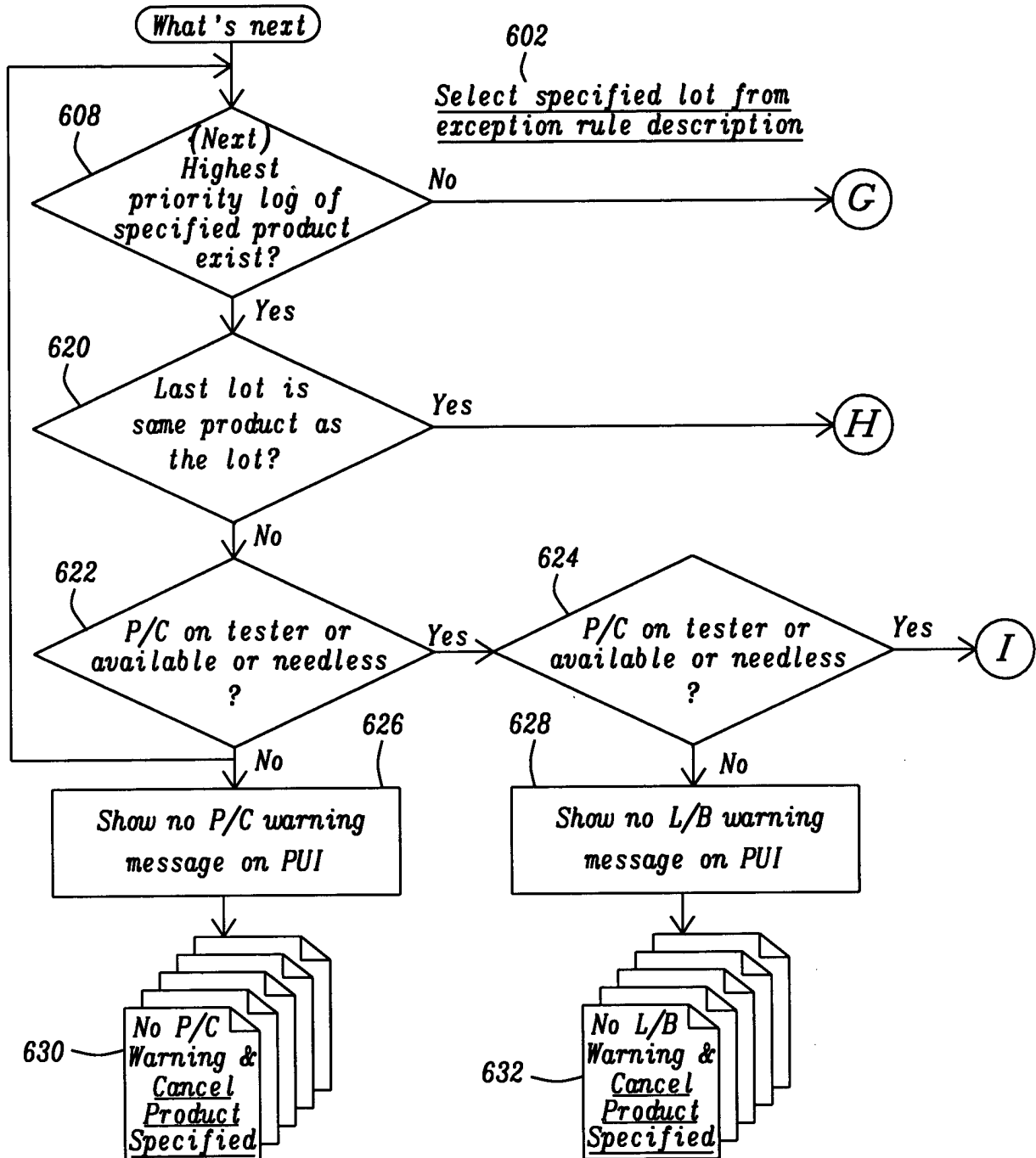


FIG. 6A

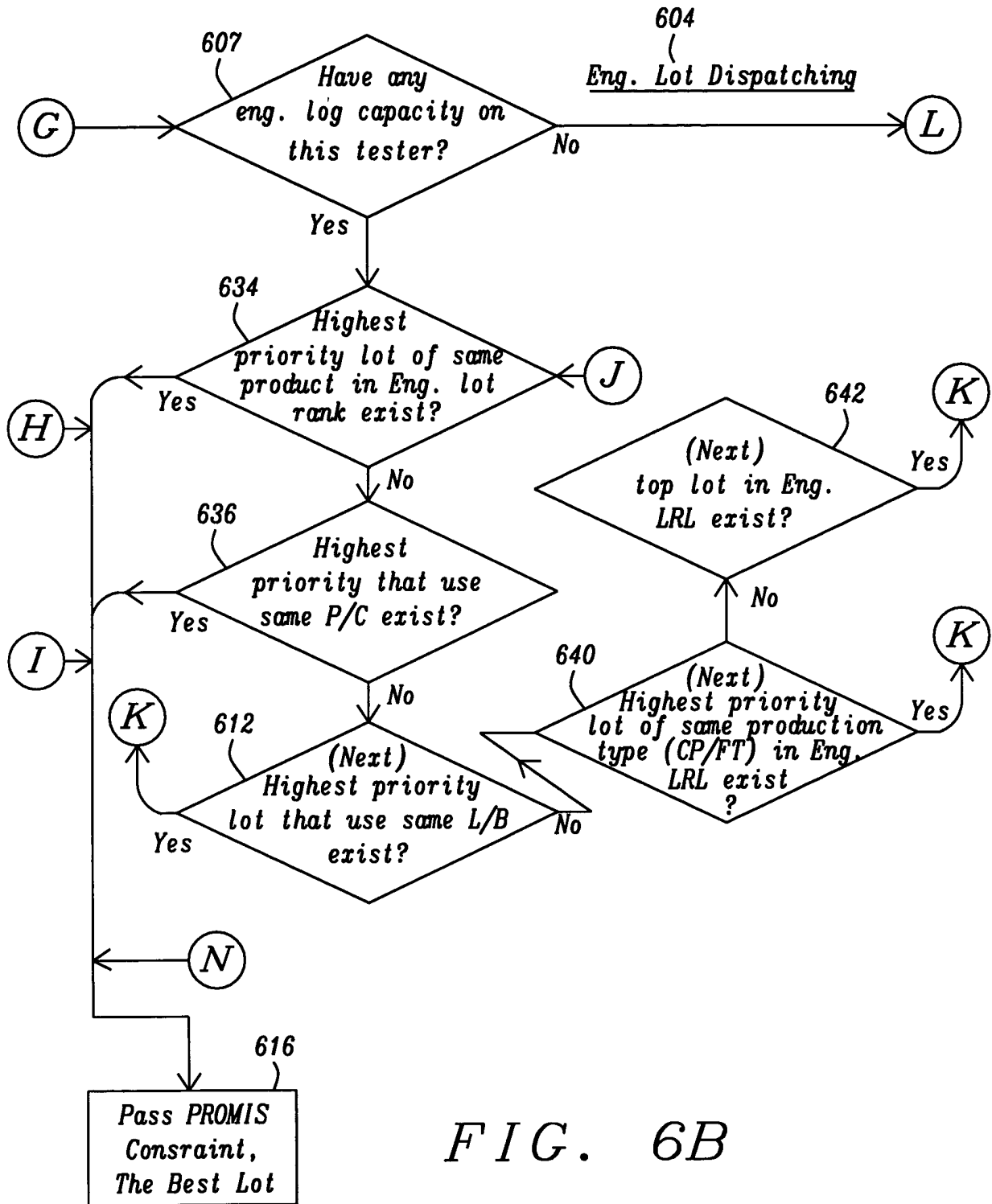


FIG. 6B

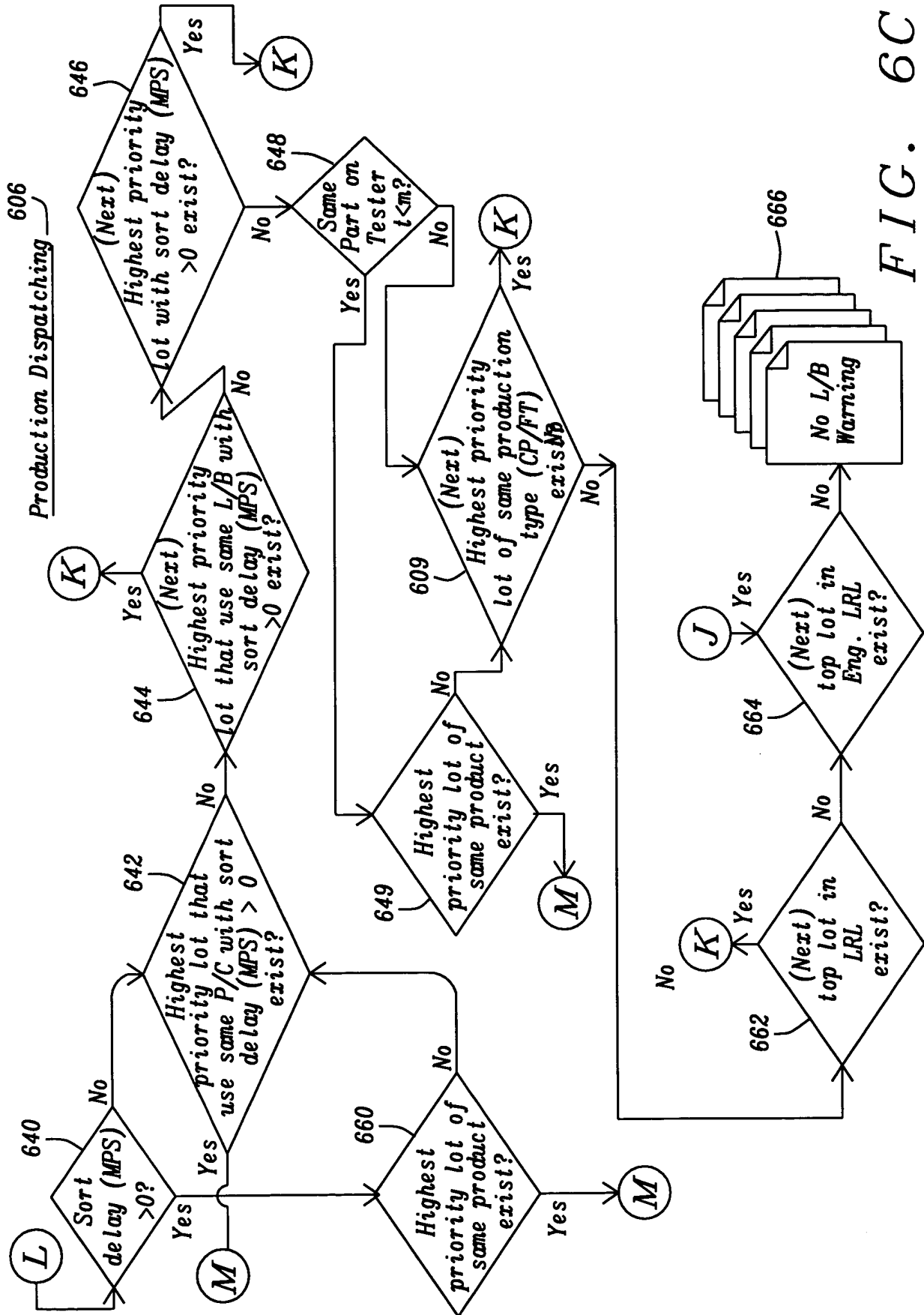


FIG. 6C

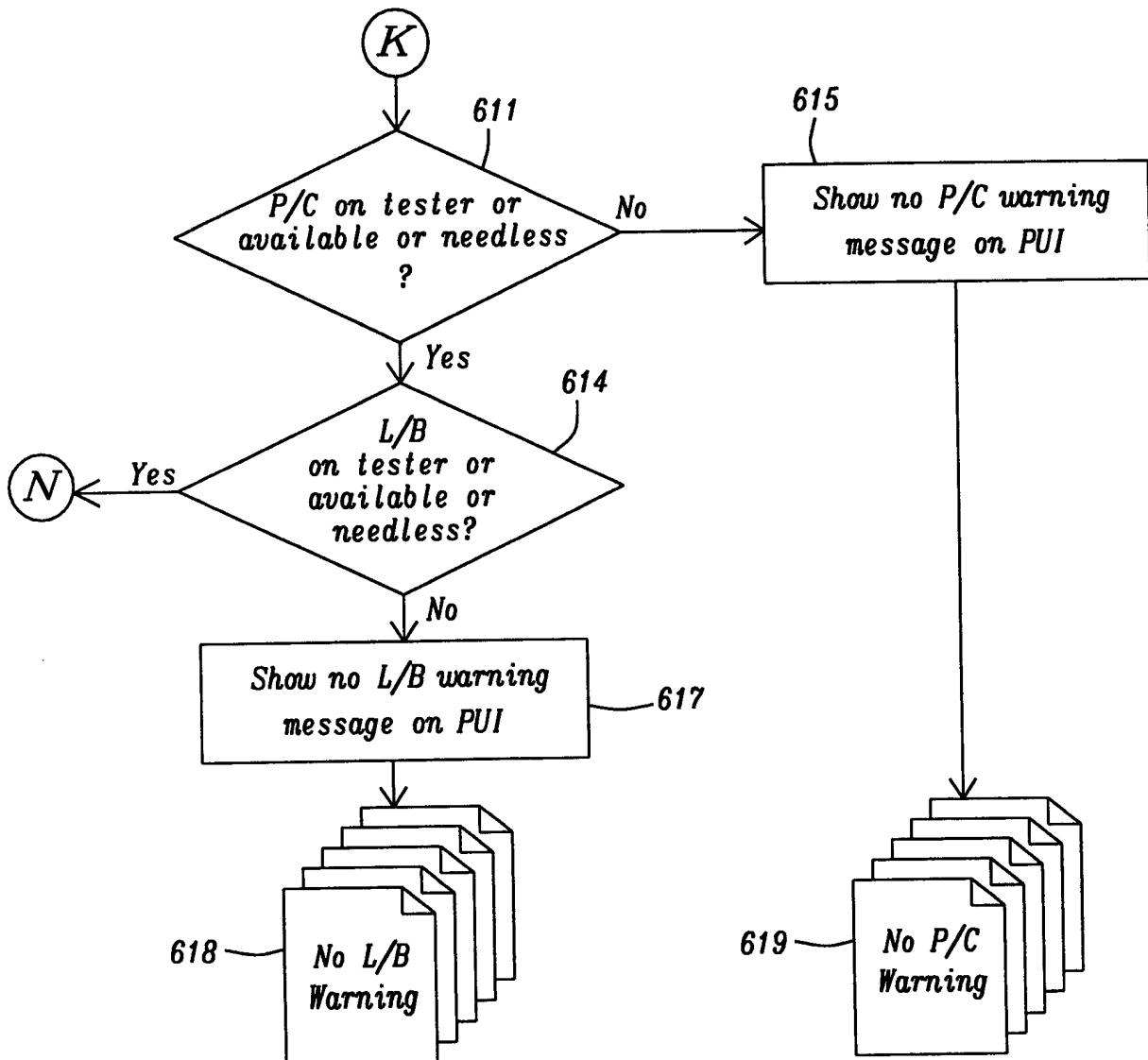


FIG. 6D